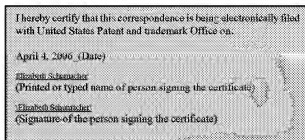


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Samir Chaudhry, *et al.*  
Serial No.: 10/762,788  
Filed: January 22, 2004  
Title: **MOS TRANSISTOR AND METHOD OF MANUFACTURE**  
Grp./A.U.: 2814  
Examiner: Long Pham

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Sir:

**REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111**

In response to the Examiner's Action mailed January 4, 2006, please reconsider the above-identified application for the following reasons: